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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/662,309	09/16/2003	Ryo Fukuda	242278US-620-39-2-S	1787	

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING LATCHING MEANS CAPABLE OF SCANNING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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EXAMINER		ART UNIT		CLASS-SUBCLASS			
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